

SESSION 1.9

CALTRANS 2070 TESTING FOR QPL ACCEPTANCE

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2070 & ITS CABINET WORKSHOP - AUGUST 2001

2070 TESTING

- **PHYSICAL INSPECTION**
- **SOFTWARE INSPECTION**
- **DIAGNOSTIC ACCEPTANCE TESTS**
- **POWER SUPPLY TESTS**
- **ENVIRONMENTAL TESTS**
- **ETHERNET**

PHYSICAL INSPECTION

- **ENSURE ALL DELIVERABLES ARE WITH CONTROLLER**
- **PHYSICAL DIMENSIONS ARE CORRECT**
- **COMPONENTS ARE PROPERLY LABELED**
- **PARTS ARE NO OLDER THAN 3 YEARS**

SOFTWARE INSPECTION

- **ENSURE ALL SOFTWARE MODULES ARE PRESENT**
- **DAYLIGHT SAVINGS**
- **OS-9 VERSION**
- **MEMORY**
- **BOOT UP TIME**
- **TEST ASYNCHRONOUS COMMUNICATION ON EIA-232 AND 485 PORTS**

DIAGNOSTIC ACCEPTANCE TESTS

- **SERIAL PORT LOOPBACK**
- **FIELD IO LOOPBACK**
- **MEMORY (FLASH/DRAM/RAM)**
- **REAL TIME CLOCK**

POWER SUPPLY TESTS

- **POWER SUPPLY VOLTAGES AND LOAD TESTING**
- **ACFAIL AND SYSRESET TIMING SIGNALS**
- **EFFICIENCY**
- **LINESYNC**
- **SHORT OUT**

ENVIRONMENTAL TESTS

- **LOAD AND RUN CALTRANS TRAFFIC CONTROL SIGNAL PROGRAM**
- **TEMPERATURE TESTING +74 C AND -37 C**
- **2 KVA TEST**
- **FRONT PANEL KEYBOARD TEST**

ETHERNET - 2070 LITE ONLY

- **TELNET TO CONTROLLER USING ETHERNET**
- **ADDITIONAL ETHERNET MODULES ARE PRESENT**